



UNI-SEMICONDUCTOR CO., LTD

宇力半导体有限公司



AP30P10 Data Sheet

V 1.1

版权归宇力半导体有限公司

主要参数 MAIN CHARACTERISTICS

ID	-35 A
VDSS	-100 V
Rdson-typ (@Vgs=10V)	32mΩ
Rdson-typ (@Vgs=4.5V)	36mΩ
Qg-typ	66nC

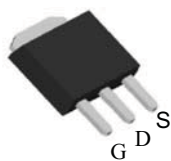
产品特性 FEATURES

低栅极电荷	Low gate charge
低 Crss (典型值 190pF)	Low Crss (typical 190pF)
开关速度快	Fast switching
100%经过雪崩测试	100% avalanche tested
高抗 dv/dt 能力	Improved dv/dt capability
RoHS 产品	RoHS product

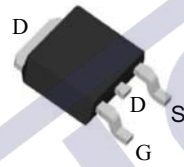
用途 APPLICATIONS

负载开关	Load switch
------	-------------

封装形式 Package

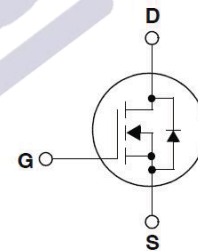


TO-251



TO-252

等效电路 Equivalent Circuit



绝对最大额定值 ABSOLUTE RATINGS (Tc=25°C)

项目 Parameter	符号 Symbol	数值 Value	单位 Unit
最高漏极-源极直流电压 Drain-Source Voltage	VDS	-100	V
连续漏极电流* Drain Current -continuous *	ID (Tc=25°C)	-35	A
	ID (Tc=100°C)	-25	A
最大脉冲漏极电流 (注 1) Drain Current - pulse (note 1)	IDM	-140	A
最高栅源电压 Gate-Source Voltage	VGS	±20	V
单脉冲雪崩能量 (注 2) Single Pulsed Avalanche Energy (note 2)	EAS	480	mJ
雪崩电流 (注 1) Avalanche Current (note 1)	IAR	17	A
重复雪崩能量 (注 1) Repetitive Avalanche Current (note 1)	EAR	16	mJ
二极管反向恢复最大电压变化速率 (注 3) Peak Diode Recovery dv/dt (note 3)	dv/dt	5.0	V/ns
耗散功率 Power Dissipation	PD (TC=25°C)	105	W
	-Derate above 25°C	0.71	W/°C
最高结温及存储温度 Operating and Storage Temperature Range	TJ, TSTG	-55~+150	°C
引线最高焊接温度 Maximum Lead Temperature for Soldering Purposes	TL	300	°C

*漏极电流由最高结温限制

*Drain current limited by maximum junction temperature

电特性 ELECTRICAL CHARACTERISTICS

项目 Parameter	符号 Symbol	测试条件 Tests conditions	最小 Min	典型 Typ	最大 Max	单位 Units
关态特性 Off-Characteristics						
漏-源击穿电压 Drain-Source Voltage	BV _{DSS}	I _D = -250μA, V _{GS} = 0V	-100	-	-	V
击穿电压温度特性 Breakdown Voltage Temperature Coefficient	ΔBV _{DSS} /ΔT _J	I _D = -250μA, referenced to 25°C	-	-0.1	-	V/°C
零栅压下漏极漏电流 Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -100V, V _{GS} = 0V, T _C = 25°C	-	-	-1	μA
		V _{DS} = -80V, T _C = 125°C	-	-	-10	μA
栅极体漏电流 Gate-body leakage current	I _{GSS} (F/R)	V _{DS} = 0V, V _{GS} = ±20V	-	-	±100	nA
通态特性 On-Characteristics						
阈值电压 Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -250μA	-1.3	-1.95	-2.5	V
静态导通电阻 Static Drain-Source On-Resistance	R _{DS(ON)}	V _{GS} = -10V, I _D = -15A	-	32	38	mΩ
		V _{GS} = -4.5V, I _D = -10A	-	36	45	
正向跨导 Forward Transconductance	g _{fs}	V _{DS} = -5V, I _D = -12A (note 4)	-	28	-	S
动态特性 Dynamic Characteristics						
栅电阻 Gate Resistance	R _g	f = 1.0MHz, V _{DS} OPEN	-	1.5	-	Ω
输入电容 Input capacitance	C _{iss}	V _{DS} = -25V, V _{GS} = 0V, f = 1.0MHz	-	3100	-	pF
输出电容 Output capacitance	C _{oss}		-	360	-	
反向传输电容 Reverse transfer capacitance	C _{rss}		-	190	-	
开关特性 Switching Characteristics						
延迟时间 Turn-On delay time	t _{d(on)}	V _{DS} = -50V, I _D = -35A, R _G = 4.7Ω V _{GS} = -10V (note 4, 5)	-	15	-	ns
上升时间 Turn-On rise time	t _r		-	17	-	ns
延迟时间 Turn-Off delay time	t _{d(off)}		-	31	-	ns
下降时间 Turn-Off Fall time	t _f		-	53	-	ns
栅极电荷总量 Total Gate Charge	Q _g		V _{DS} = -80V, I _D = -35A, V _{GS} = -10V (note 4, 5)	-	66	-
栅-源电荷 Gate-Source charge	Q _{gs}		-	17	-	nC
栅-漏电荷 Gate-Drain charge	Q _{gd}		-	23	-	nC
漏-源二极管特性及最大额定值 Drain-Source Diode Characteristics and Maximum Ratings						
正向最大连续电流 Maximum Continuous Drain -Source Diode Forward Current		I _S	-	-	-35	A
正向最大脉冲电流 Maximum Pulsed Drain-Source Diode Forward Current		I _{SM}	-	-	-140	A
正向压降 Drain-Source Diode Forward Voltage	V _{SD}	V _{GS} = 0V, I _S = -35A	-	-0.82	-1.3	V
反向恢复时间 Reverse recovery time	t _{rr}	V _{GS} = 0V, I _S = -35A, dI _F /dt = 100A/μs (note 4)	-	52	-	ns
反向恢复电荷 Reverse recovery charge	Q _{rr}		-	96	-	nC

热特性 THERMAL CHARACTERISTIC

项目 Parameter	符号 Symbol	最大值 Max	单位 Unit
结到管壳的热阻 Thermal Resistance, Junction to Case	Rth(j-c)	1.45	°C/W
结到环境的热阻 Thermal Resistance, Junction to Ambient	Rth(j-A)	110	°C/W

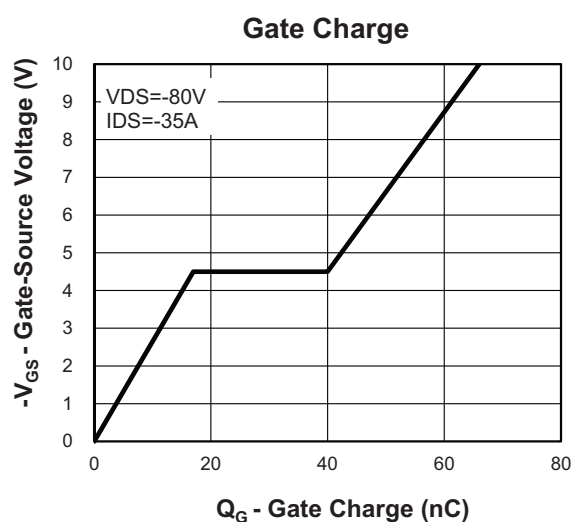
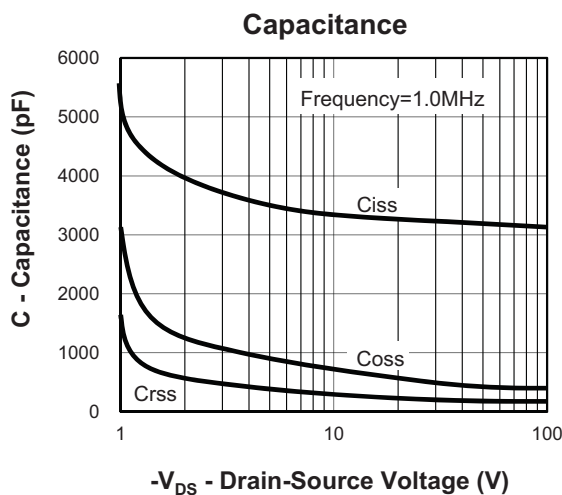
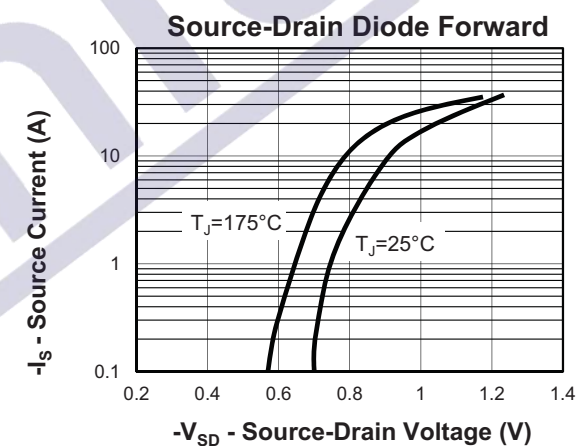
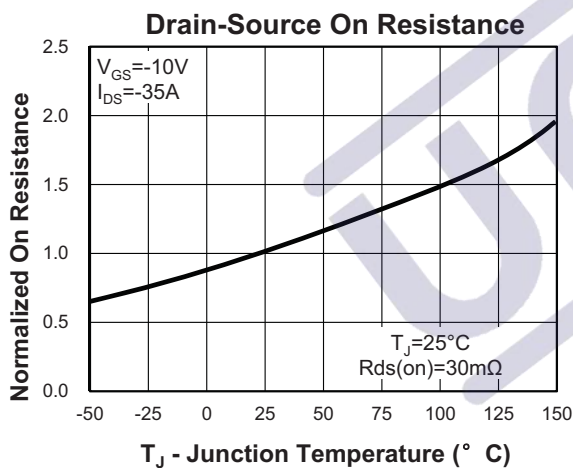
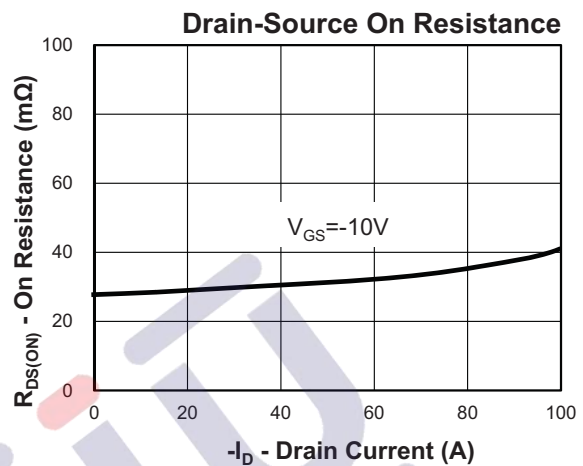
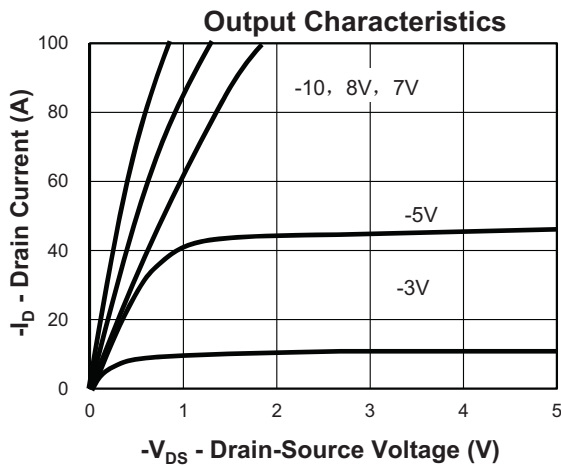
注释:

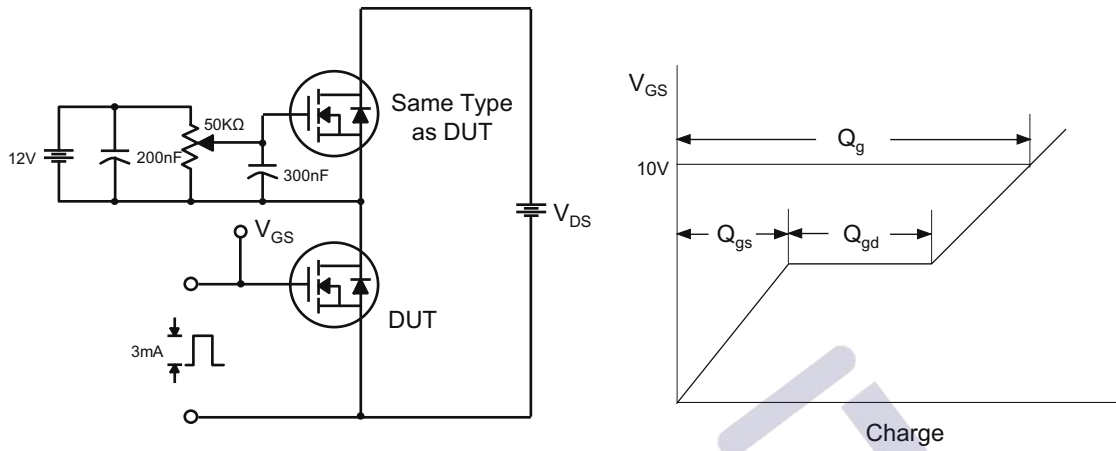
- 1: 脉冲宽度由最高结温限制
- 2: L=1mH, IAS=-17A, VDD=-48V, RG=25 Ω, 起始结温 TJ=25°C
- 3: ISD ≤25A, di/dt ≤300A/μs, VDD≤BVDS, 起始结温 TJ=25°C
- 4: 脉冲测试: 脉冲宽度 ≤300μs, 占空比≤2%
- 5: 基本与工作温度无关

Notes:

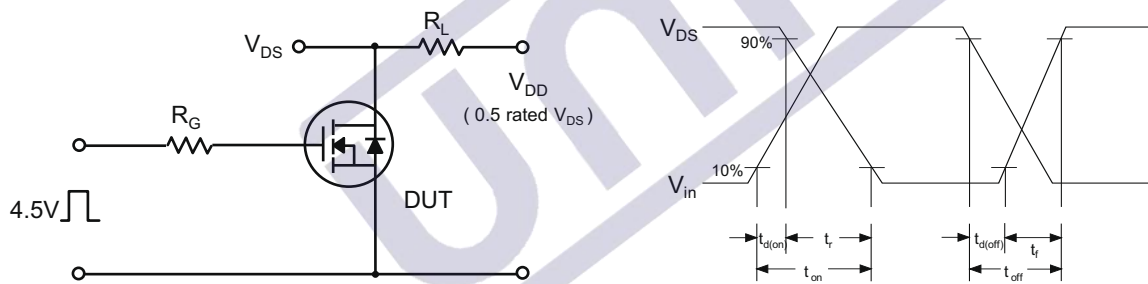
- 1: Pulse width limited by maximum junction temperature
- 2: L=1mH, IAS=-17A, VDD=-48V, RG=25 Ω, Starting TJ=25°C
- 3: ISD ≤25A, di/dt ≤300A/μs, VDD≤BVDS, Starting TJ=25°C
- 4: Pulse Test: Pulse Width ≤300μs, Duty Cycle≤2%
- 5: Essentially independent of operating temperature

Typical Characteristics

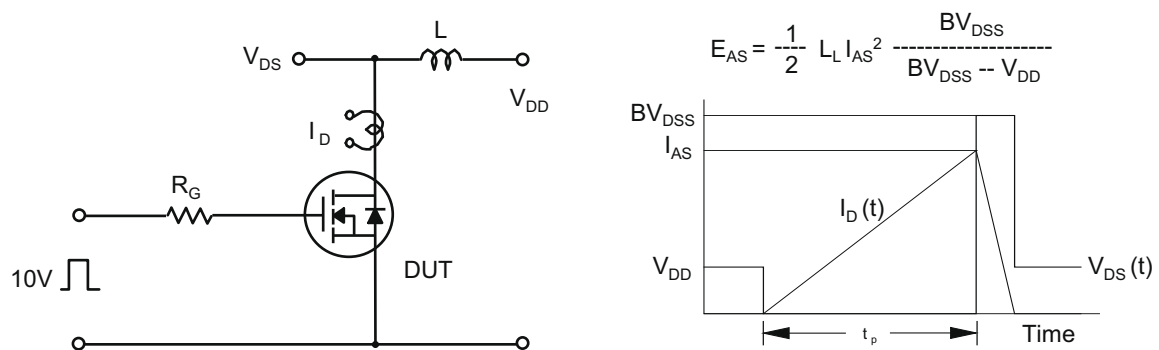




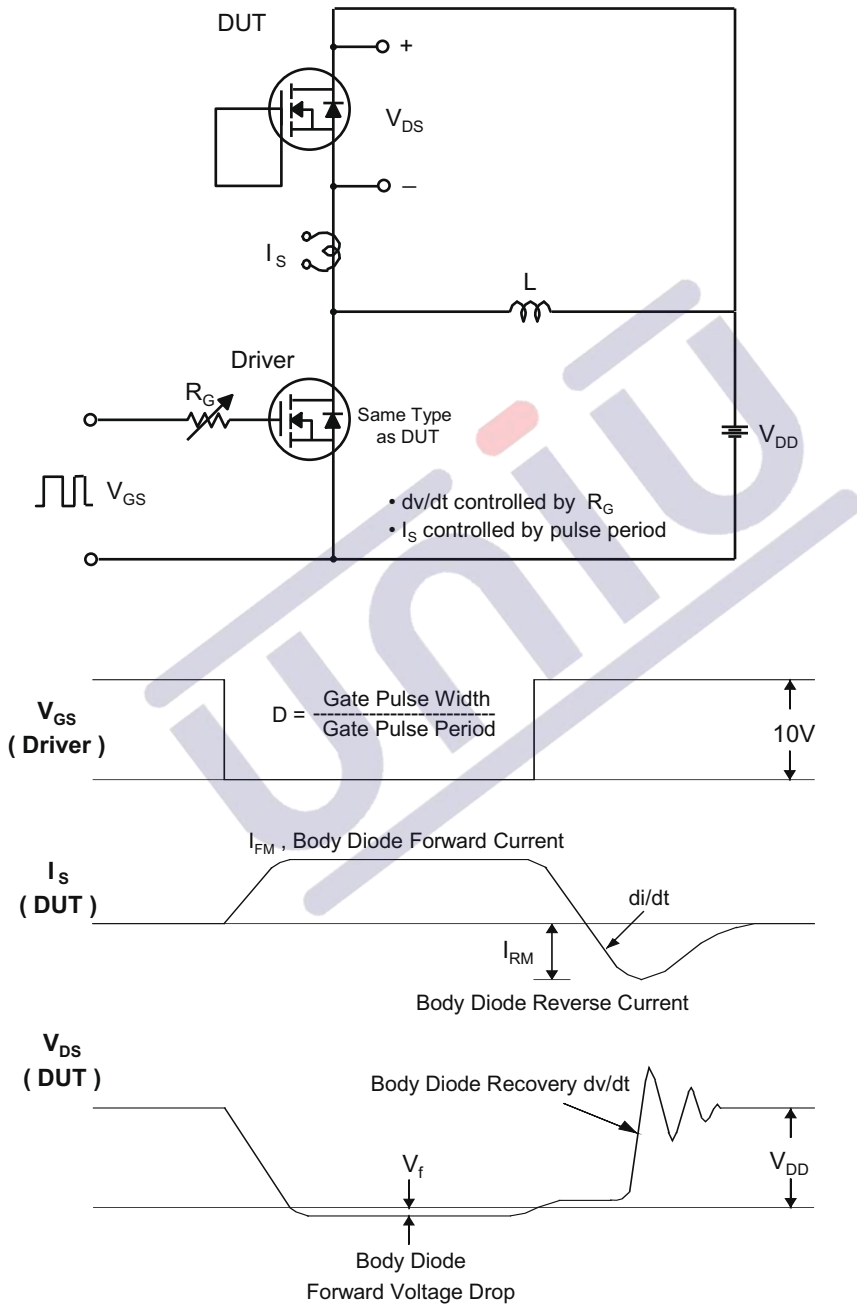
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms

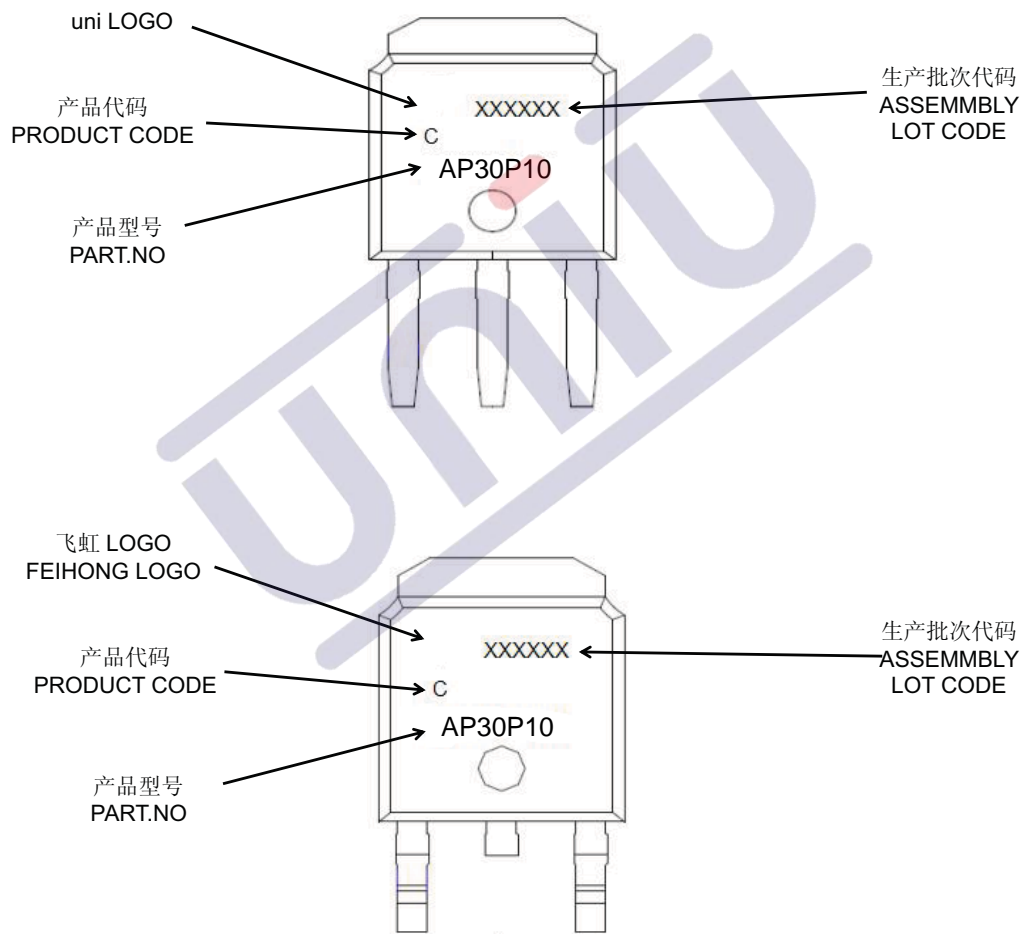


Unclamped Inductive Switching Test Circuit & Waveforms



Peak Diode Recovery dv/dt Test Circuit & Waveforms

印记 Marking:



1.版本记录

DATE	REV.	DESCRIPTION
2018/07/14	1.0	First Release
2019/07/13	1.1	Layout adjustment

2.免责声明

浙江宇力微新能源科技有限公司保留对本文档的更改和解释权力，不另行通知！客户在下单前应获取我司最新版本资料，并验证相关信息是否最新和完整。量产方案需使用方自行验证并自担所有批量风险责任。未经我司授权，该文件不得私自复制和修改。产品不断提升，以追求高品质、稳定性强、可靠性高、环保、节能、高效为目标，我司将竭诚为客户提供性价比高的系统开发方案、技术支持等更优秀的服务。

版权所有 浙江宇力微新能源科技有限公司/绍兴宇力半导体有限公司

3.联系我们

浙江宇力微新能源科技有限公司

总部地址：绍兴市越城区斗门街道袍渎路25号中节能科创园45幢4/5楼电话：

0575-85087896（研发部）

传真：0575-88125157

E-mail: htw@uni-semic.com

无锡地址：无锡市锡山区先锋中路6号中国电子（无锡）数字芯城1#综合楼503室

电话：0510-85297939

E-mail: zh@uni-semic.com

深圳地址：深圳市宝安区西乡街道南昌社区宝源路泳辉国际商务大厦410

电话：0755-84510976

E-mail: htw@uni-semic.com